

**SYSTEM AND METHOD FOR ADAPTIVE PERFORMANCE  
OPTIMIZATION OF DATA PROCESSING SYSTEMS**

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**RELATED APPLICATIONS**

The present application is related to co-pending and commonly assigned U.S. Patent  
~~Application Serial No. 09/534,921~~, <sup>6,647,514</sup> entitled "IMPROVING HOST I/O PERFORMANCE  
AND AVAILABILITY OF A STORAGE ARRAY DURING REBUILD BY  
PRIORITIZING I/O REQUESTS," filed March 23, 2000; U.S. Application Serial No.  
09/680,542, entitled "METHOD FOR TIMELY DATA MIGRATION IN A REDUNDANT  
HIERARCHIC DATA STORAGE SYSTEM", filed October 4, 2000; <sup>now abandoned</sup> U.S. Patent 5,392,244,  
entitled "MEMORY SYSTEMS WITH DATA STORAGE REDUNDANCY  
MANAGEMENT," issued February 21, 1995; and U.S. Patent 5,542,065, entitled  
"METHODS FOR USING NON-CONTIGUOUSLY RESERVED STORAGE SPACE FOR  
DATA MIGRATION IN A REDUNDANT HIERARCHIC DATA STORAGE SYSTEM,"  
issued July 30, 1996, the disclosures of which are hereby incorporated herein by reference.

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**TECHNICAL FIELD**

The present invention relates in general to allocating tasks to a plurality of processing components and in particular to allocating processing power in a data storage system.